

This cross-sectional view shows a central channel region 102 flanked by side regions 104 and 106. A gate stack 108 is positioned on top of the channel. The device is supported by a substrate 110 with a base layer 112. Various contact regions and layers are labeled with reference numerals 101 through 110.

FIG. 11B
(Pulse Act)

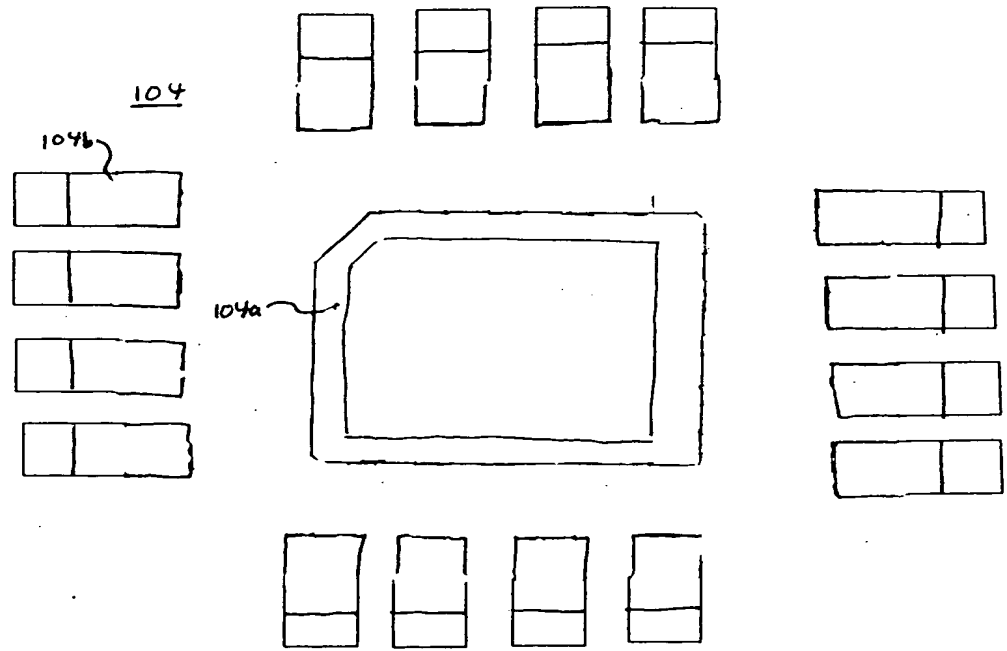


FIG. 1C
 (Prior Art)

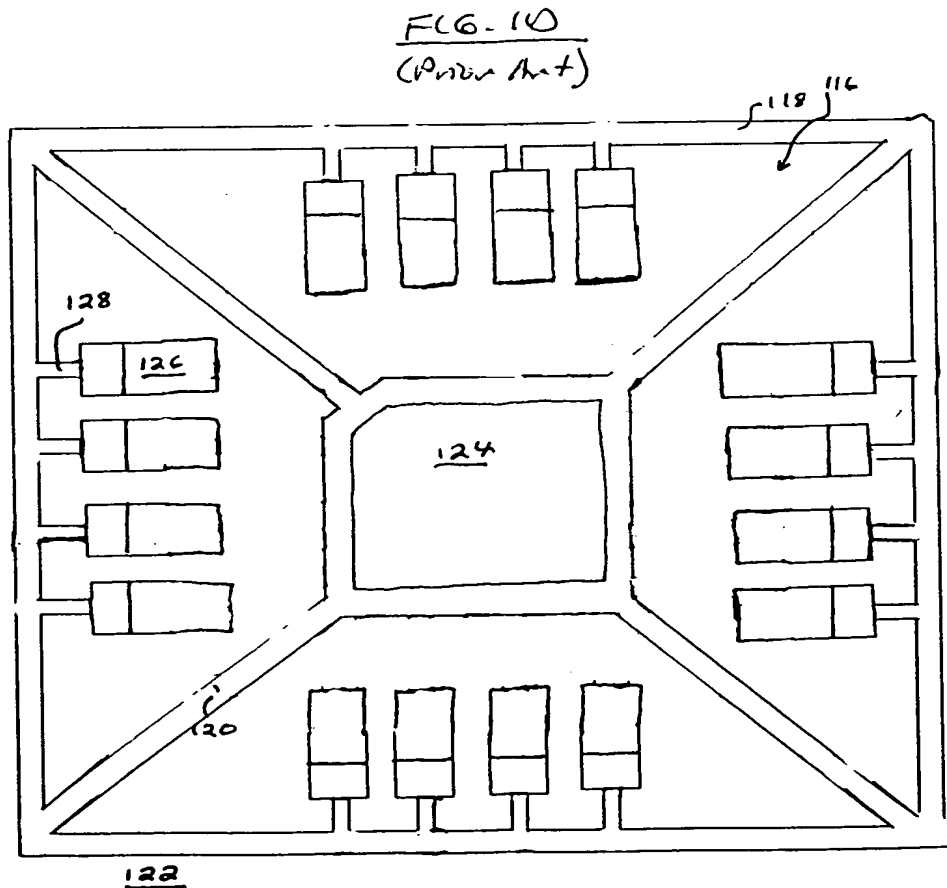


FIG. 1D
 (Prior Art)

FIG. 1E
(Prior Art)

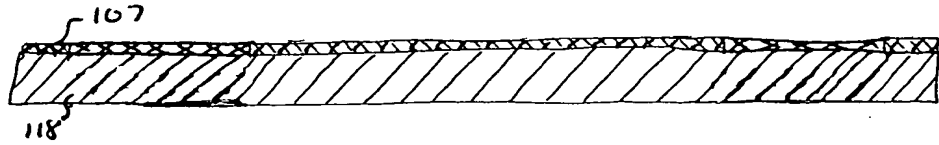


FIG. 1F
(Prior Art)

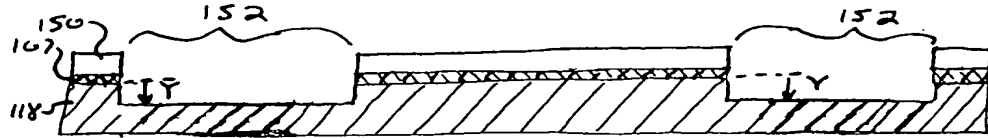


FIG. 1G
(Prior Art)

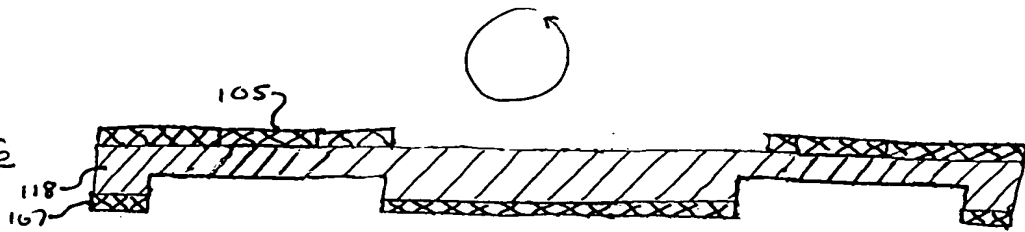
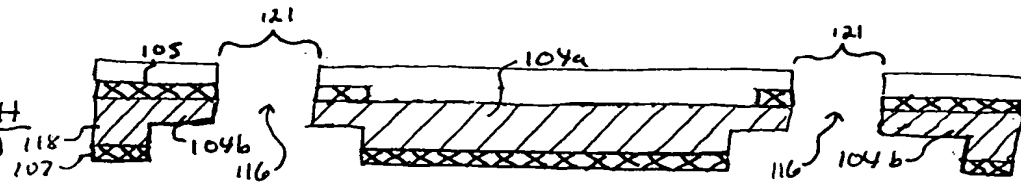


FIG. 1H
(Prior Art)



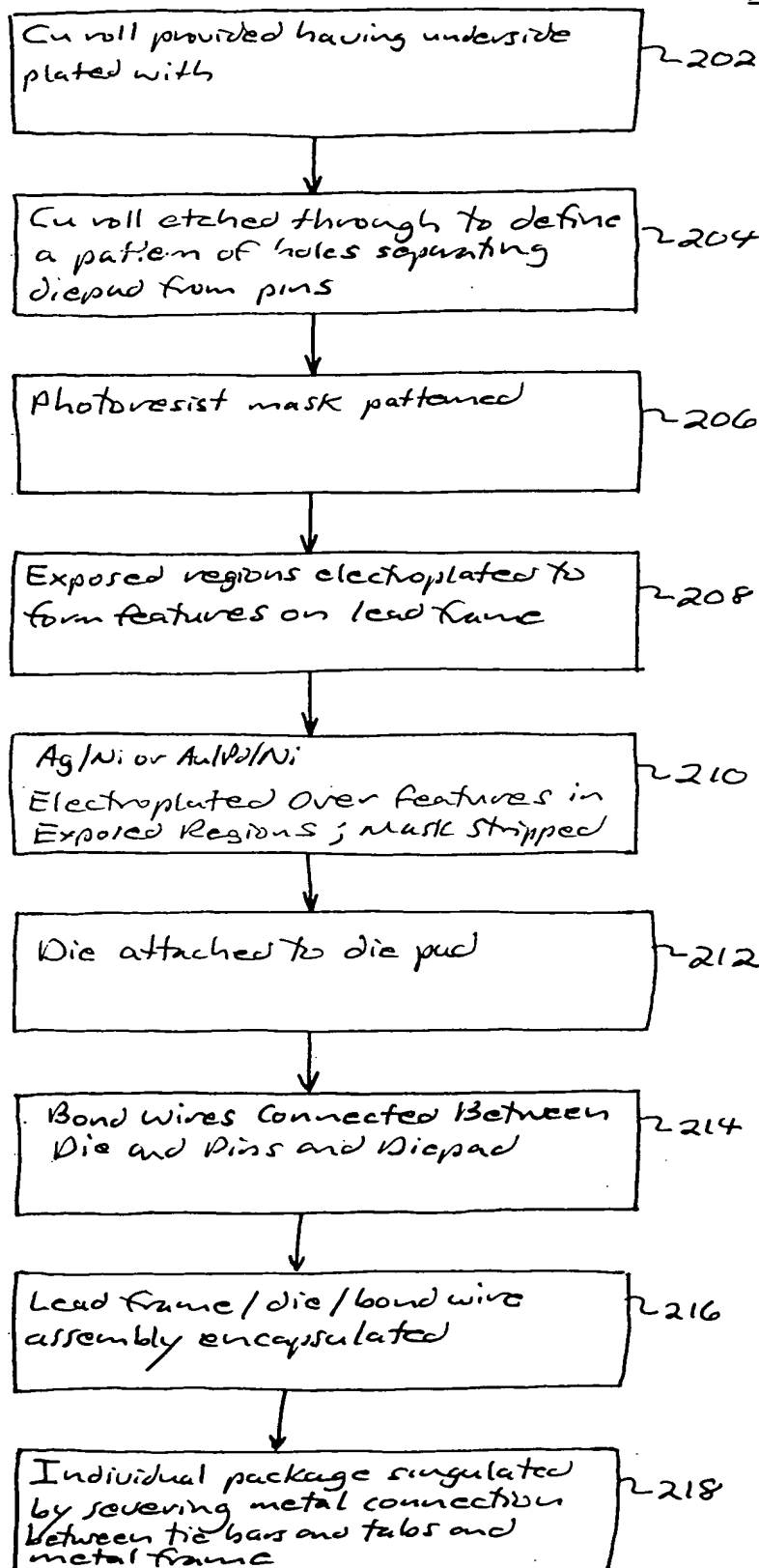


FIG. 2

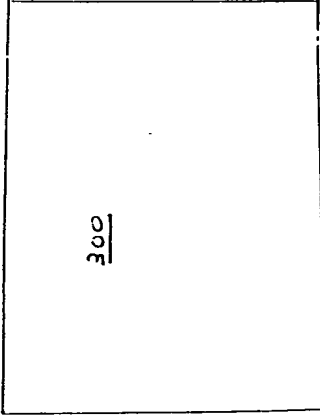


FIG. 3AA

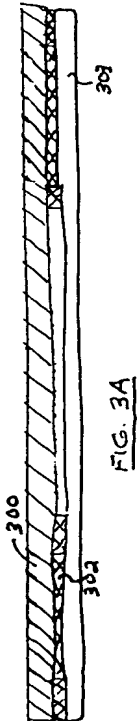


FIG. 3A

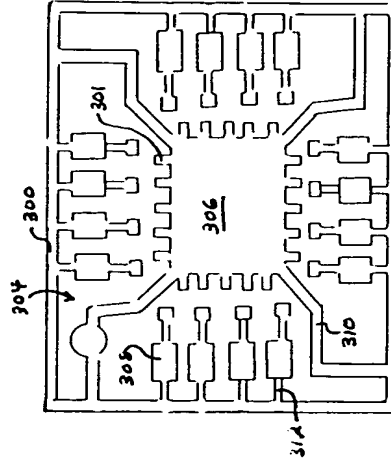


FIG. 3BA

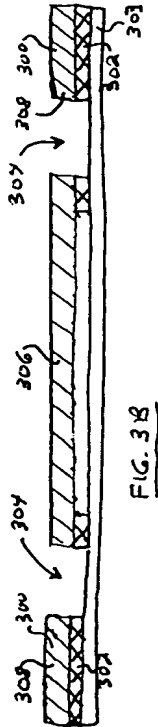


FIG. 3B

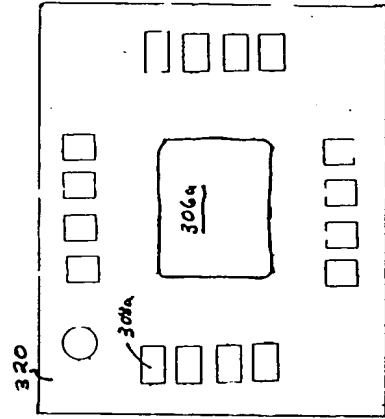


FIG. 3CA

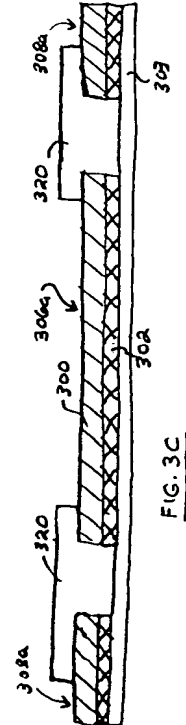


FIG. 3C

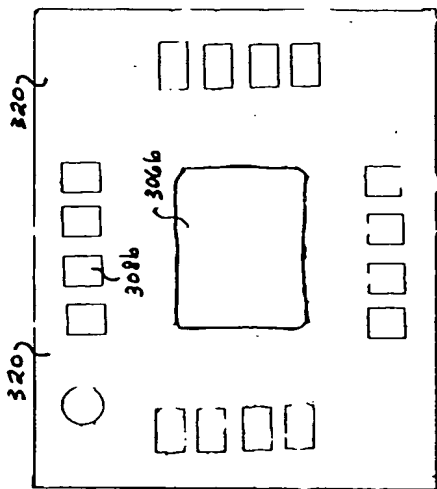


FIG. 30A

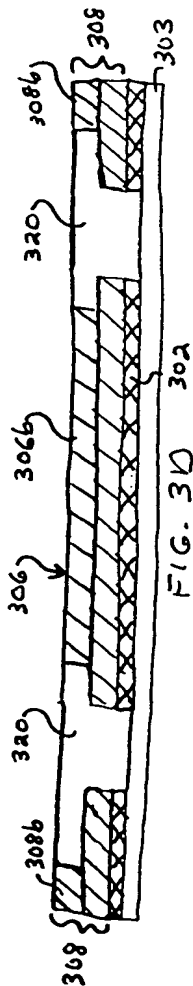


FIG. 31D

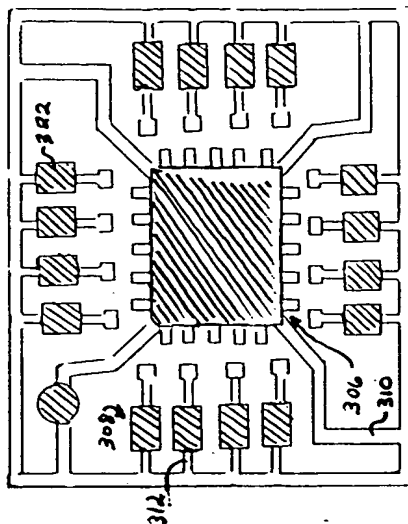


FIG. 31E

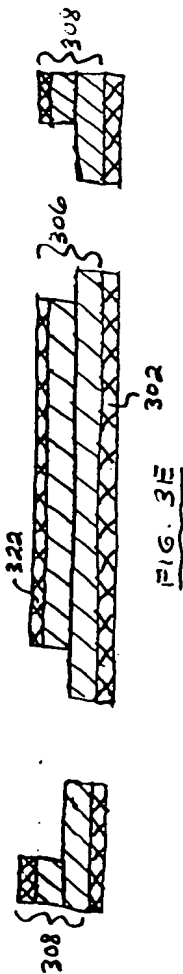


FIG. 31F

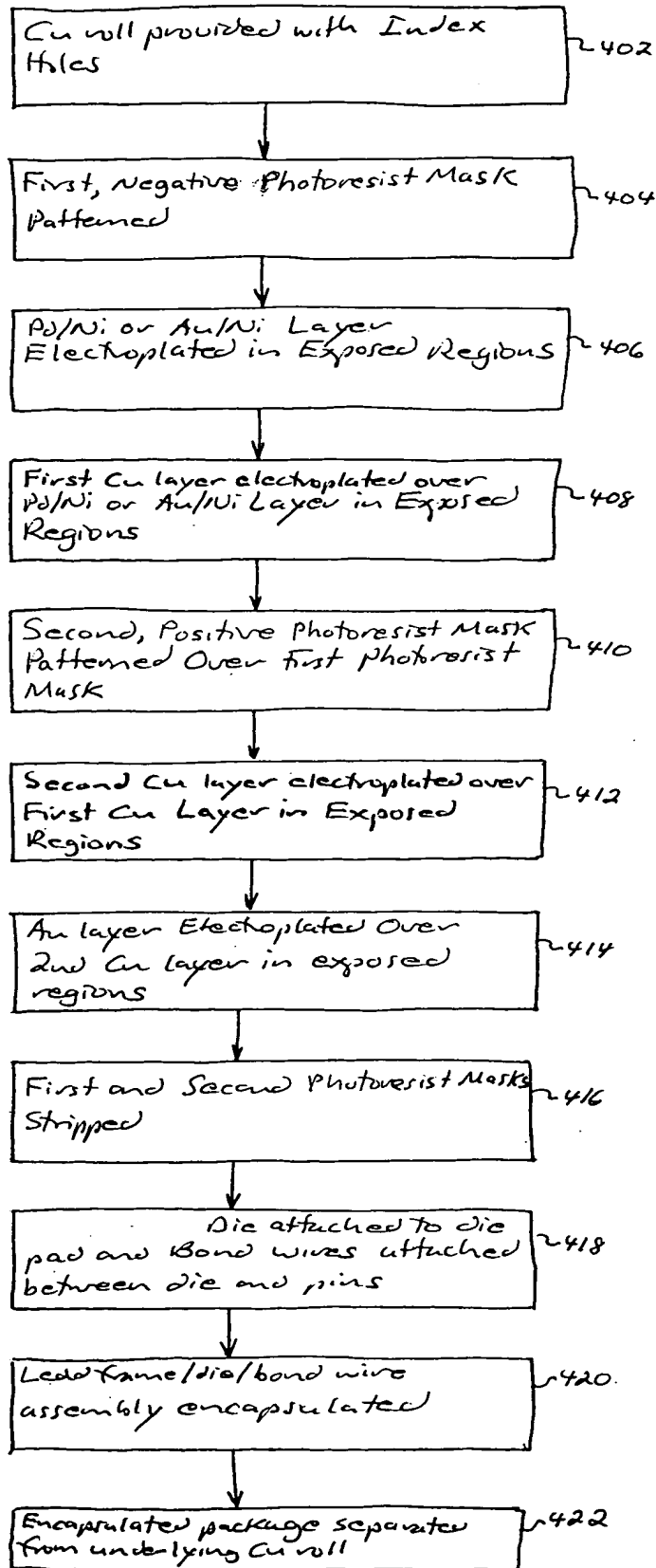


FIG. 4

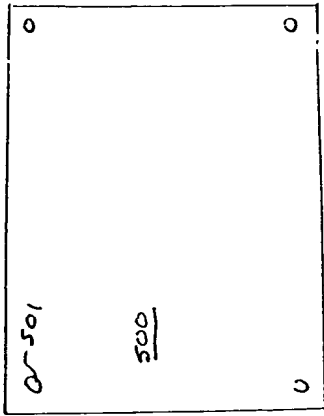


FIG. 5A

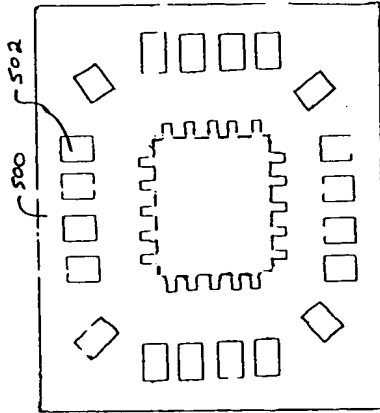


FIG. 5B

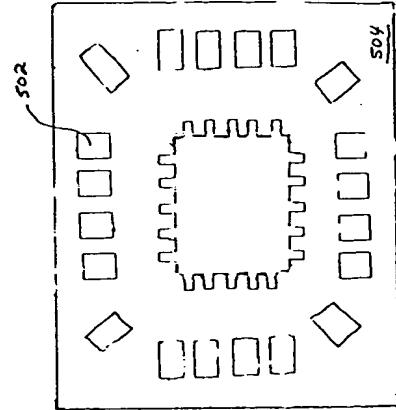


FIG. 5C



FIG. 5A



FIG. 5B

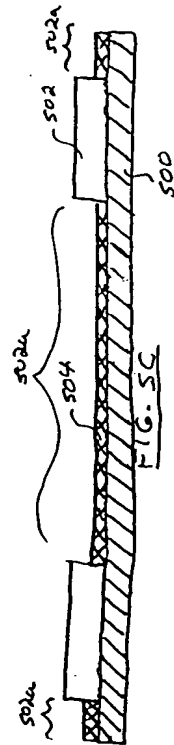


FIG. 5C

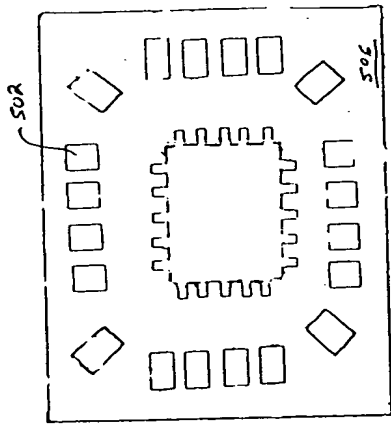


FIG. 50A

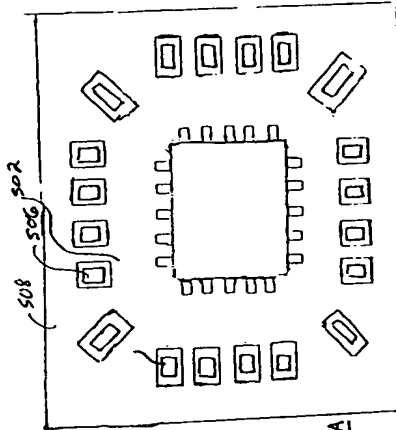


FIG. 50B

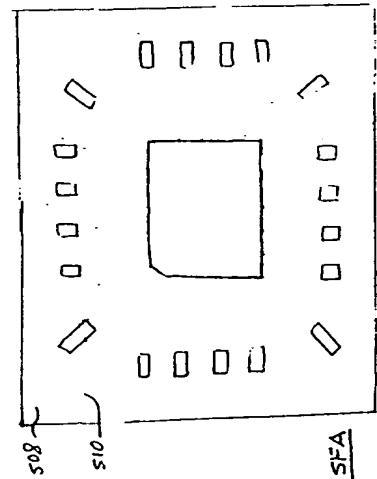


FIG. 50C

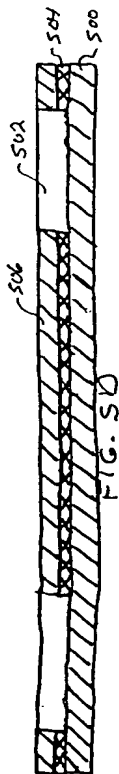


FIG. 50D

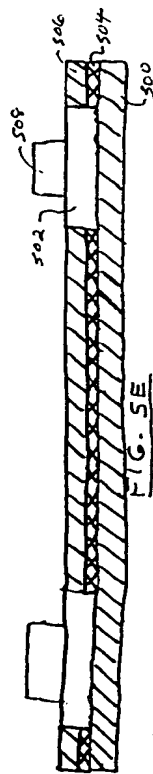


FIG. 50E

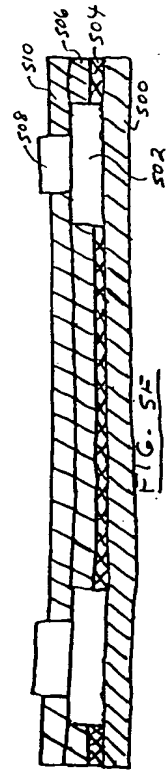
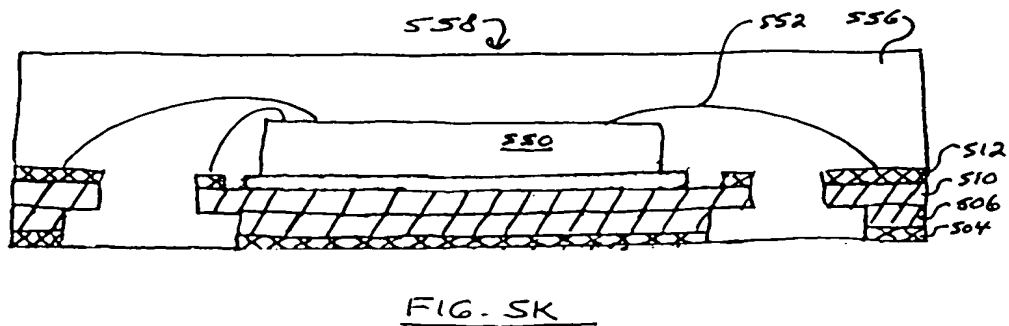
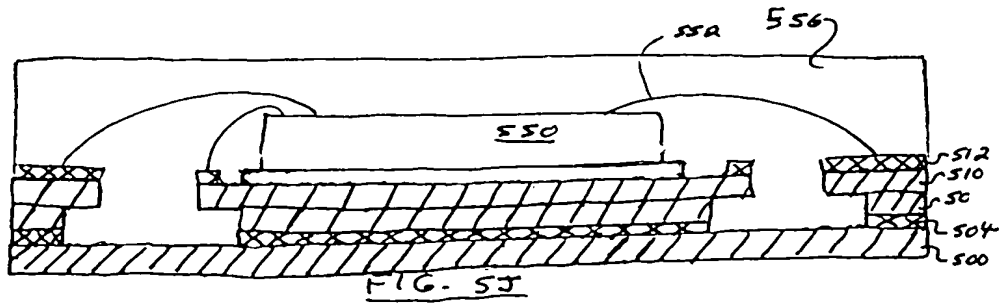
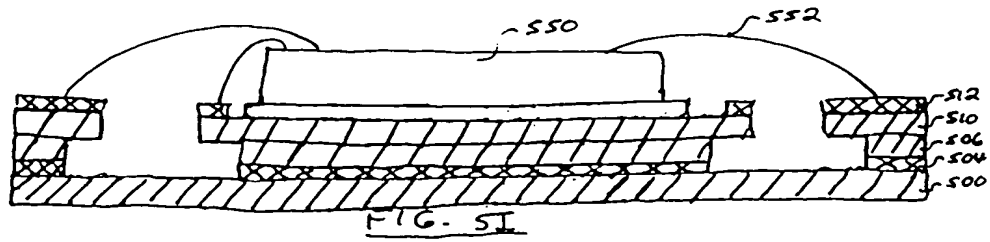
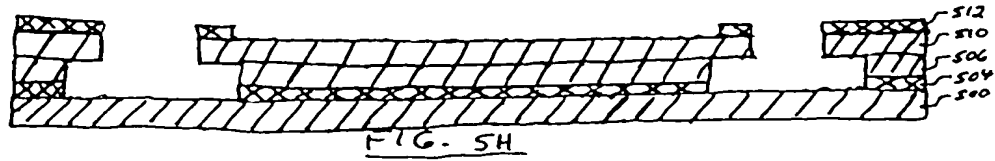
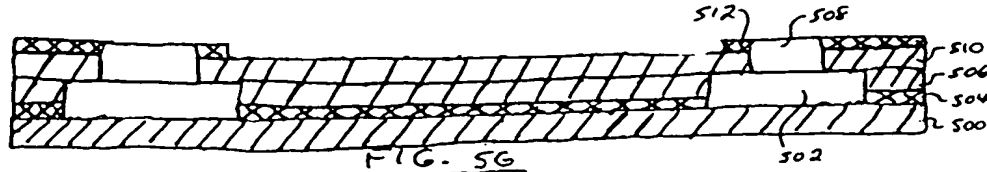


FIG. 50F



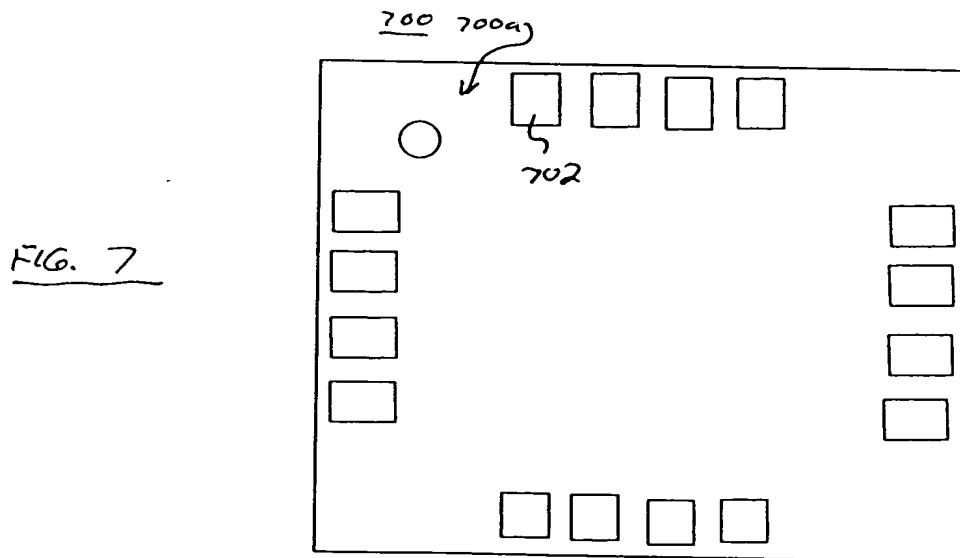
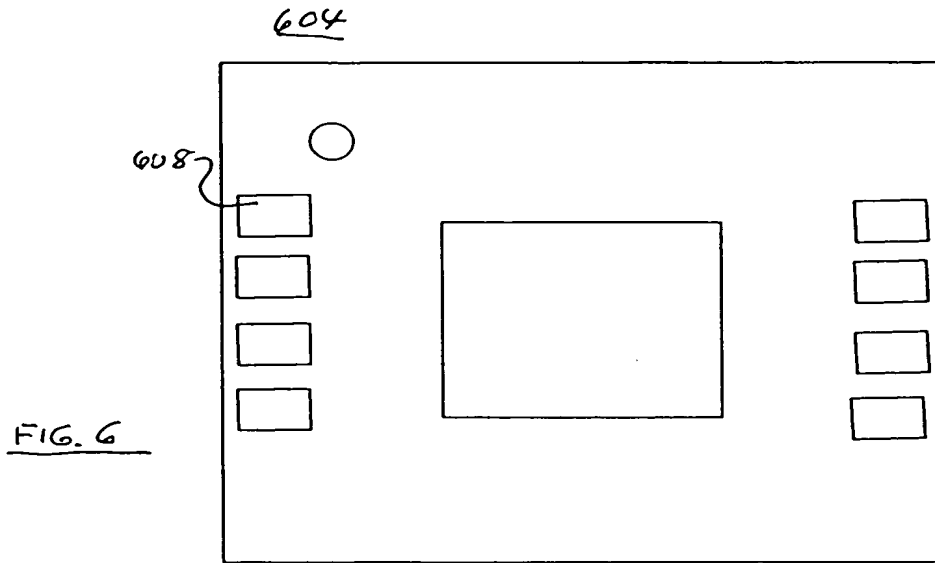


FIG. 7A

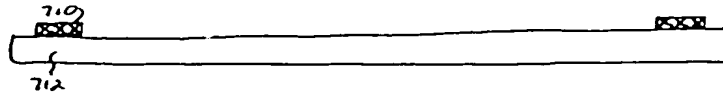


FIG. 7B

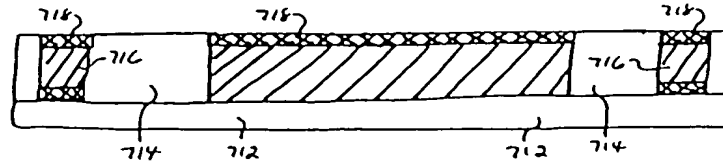


FIG. 7C

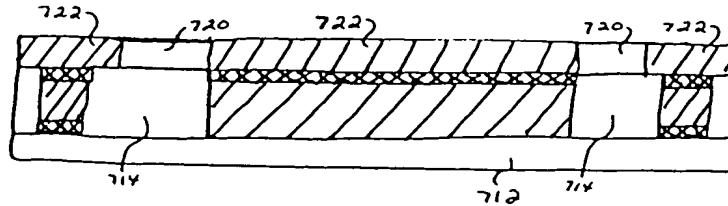


FIG. 7D

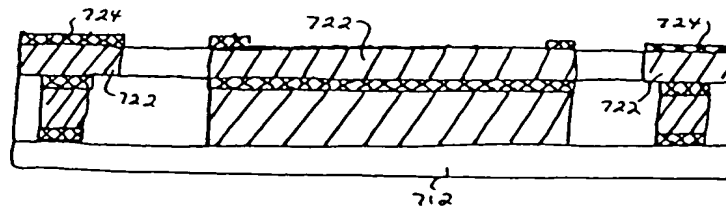


FIG. 7E

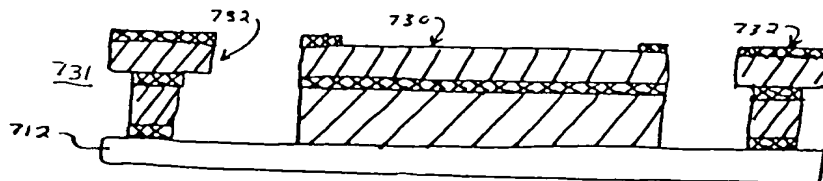


FIG. 7F

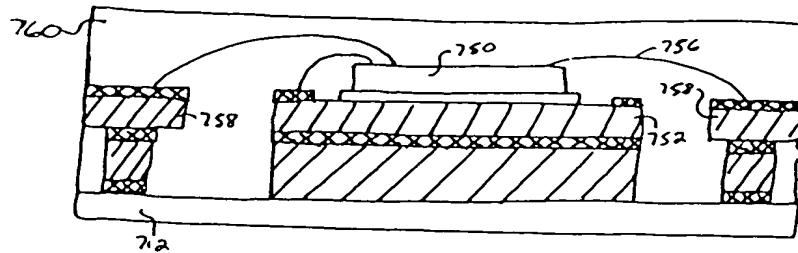


FIG. 7G

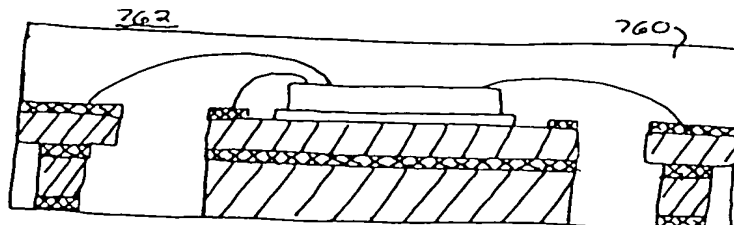
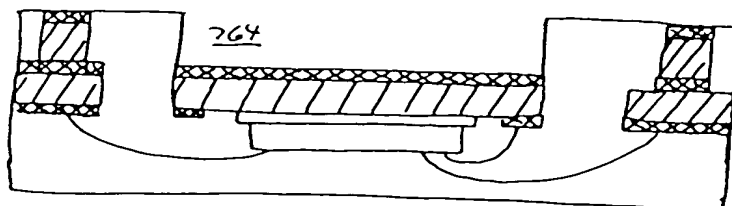


FIG. 7H



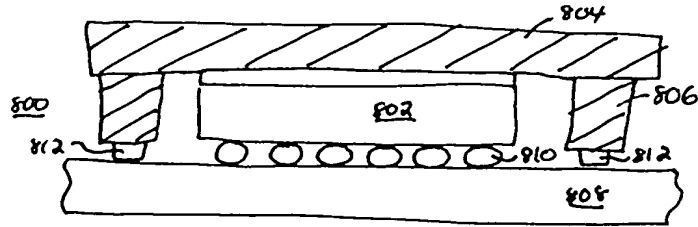


FIG. 9

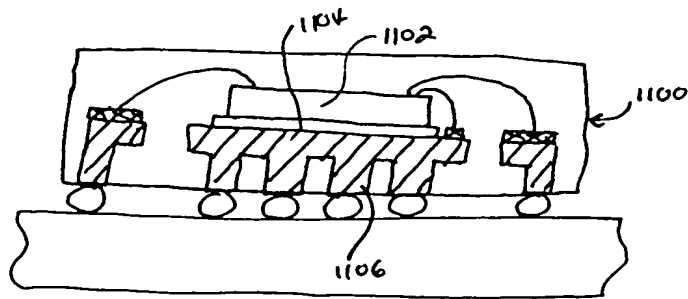


FIG. 11

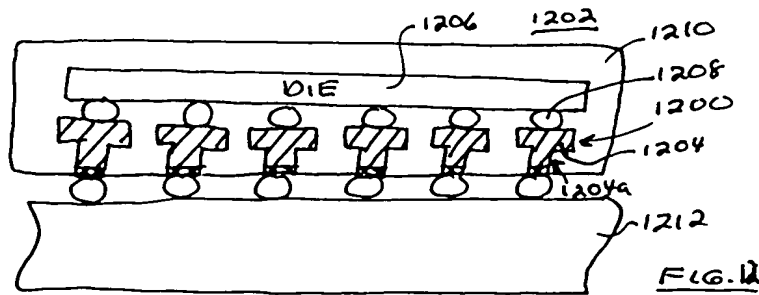


FIG. 12

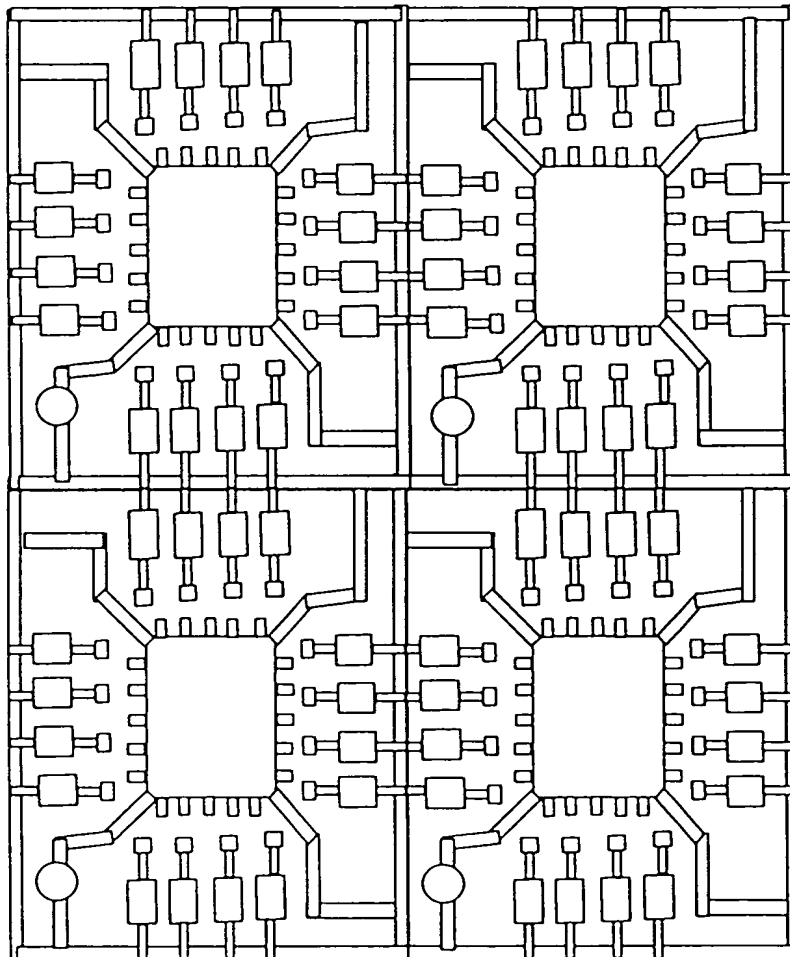


FIG. 9A

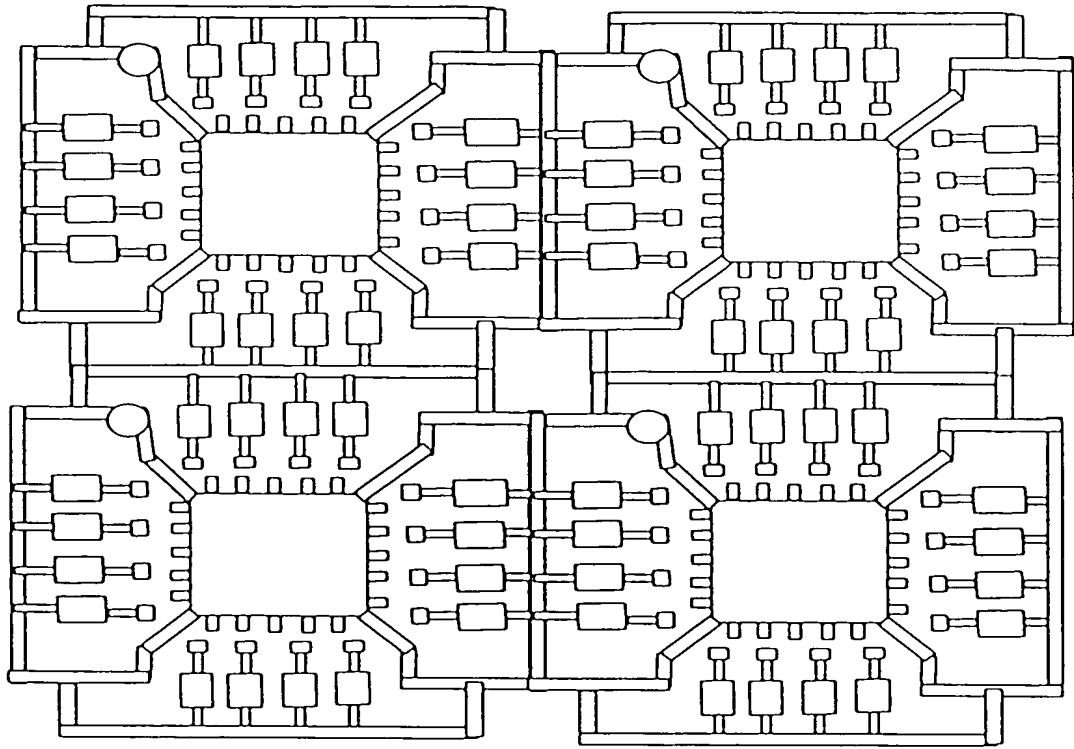


FIG. 9B

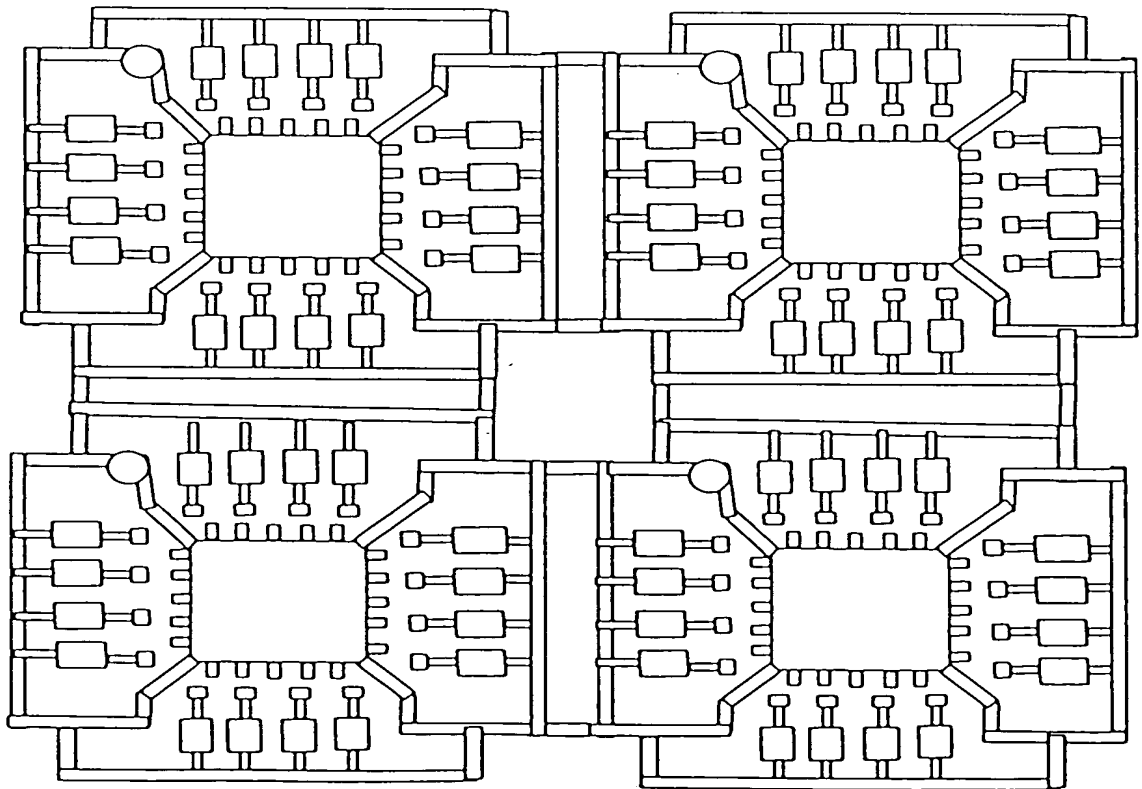


FIG. 9C

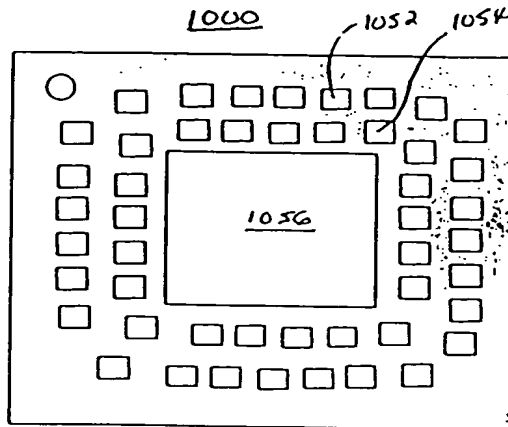


FIG. 10A

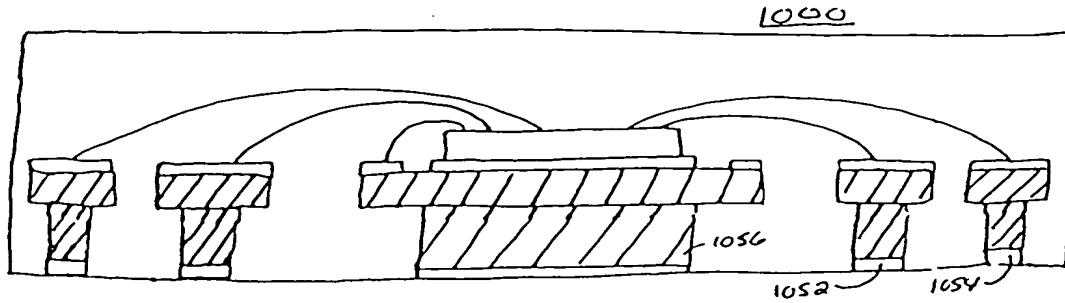


FIG. 10B

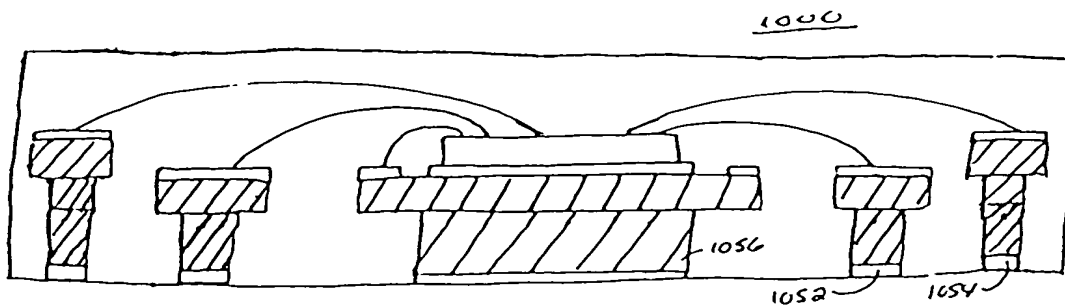


FIG. 10C

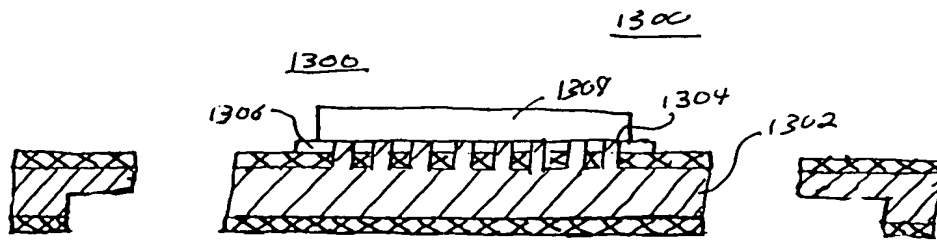


FIG. 13A

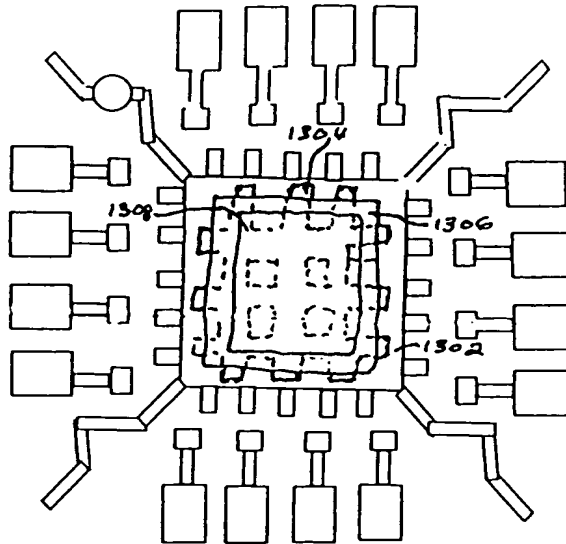


FIG. 13B